



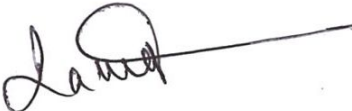
**MALINENI LAKSHMAIAH WOMEN'S ENGINEERING COLLEGE (AUTONOMOUS)**  
(Accredited by "NBA" & NAAC with A+ Grade | Approved by AICTE, New Delhi & Affiliated to  
JNTUK, Kakinada)  
Pulladigunta (Vil), Vatticherukuru (Md), Prathipadu Road, Guntur - 522 017 A.P.


Ref. No.: MLWEC/Autonomous/CoE Office/2023-24/Timetables/30

14.02.2024

**M.Tech. (VLSI) I Year I Semester (MR23 REGULATION)**  
**LAB EXTERNAL EXAMINATIONS, FEBRUARY 2024**  
**TIME TABLE**

S. No.	Date (Day)	Course Code	Lab Name
1	26.02.2024 (MONDAY)	23MTVLL01	CMOS Analog IC Design Lab
2	27.02.2024 (TUESDAY)	23MTVLL02	CMOS Digital IC Design Lab

  
**Controller of Examinations**  
CONTROLLER OF EXAMINATIONS  
MALINENI LAKSHMAIAH  
WOMEN'S ENGINEERING COLLEGE  
(AUTONOMOUS)  
Pulladigunta, GUNTUR-522017

  
**Principal**  
PRINCIPAL  
MALINENI LAKSHMAIAH  
WOMEN'S ENGINEERING COLLEGE  
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**Copy to:** ECE-HOD, Coordinator - M.Tech.(VLSI), circulation to all the I Year I Sem M.Tech.(VLSI) students and the Faculty members concerned

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